Advanced Analog IC Design

EPFL, LAUSANNE, SWITZERLAND
AUGUST 31 – SEPTEMBER 4, 2020

MONDAY, August 31

8:30-12:00 pm	MOS and Bipolar: Modes of Operation and Models	Eric Vittoz
1:30-3:00 pm	Elementary Building Blocks	Eric Vittoz
3:30-5:00 pm	Voltage References	Eric Vittoz

TUESDAY, September 1

8:30-12:00 pm & 1:30-5:00 pm	Analog Functional Blocks	Willy Sansen

---

WEDNESDAY, September 2

8:30-12:00 pm	Continuous Time Filters	Boris Murmann
1:30-5:00 pm	Switched-Capacitor Interface Circuits	Boris Murmann

---

THURSDAY, September 3

8:30-10:00 am	Switched-Capacitor Interface Circuits	Boris Murmann
10:30-12:00 pm & 1:30-5:00 pm	CMOS Off-Chip Drivers	Rinaldo Castello

---

FRIDAY, September 4

8:30-12:00 pm & 1:30-3:00 pm	Technology and other Limitations for Analog Design	Herman Casier

---